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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Yoichi YAMADA

Application No.: 09/720,142

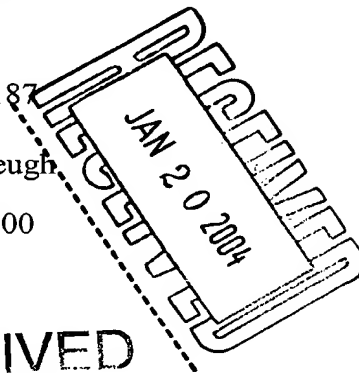
Filed: December 21, 2000

For: SEMICONDUCTOR INTEGRATED CIRCUIT

Group Art Unit: 2187

Examiner: B. Peugh

Docket No.: 107300



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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

JAN 16 2004

Technology Center 2100

Sir:

Applicant thanks Examiner Peugh for the interview on December 18, 2003. This filing constitutes Applicant's summary of the interview. Examiner Peugh agreed to withdraw all of the objections in the Office Action dated August 15, 2003 upon Applicant's filing of an Affidavit averring that Applicant is not aware of any instance of the art described in the application as "conventional" that qualifies as statutory prior art in the United States. Applicant hereby submits the Affidavit attached as requested by Examiner Peugh.

Respectfully submitted,

James A. Oliff
Registration No. 27,075

Mark R. Woodall
Registration No. 43,286

JAO:MRW/sqb

Attachment:
Affidavit

Date: January 15, 2004

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